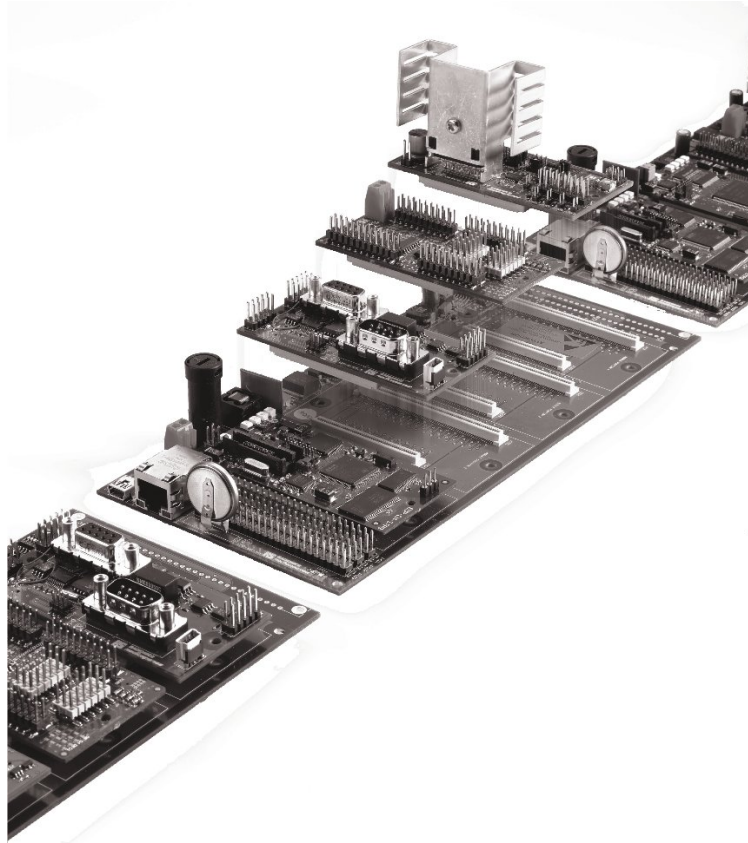
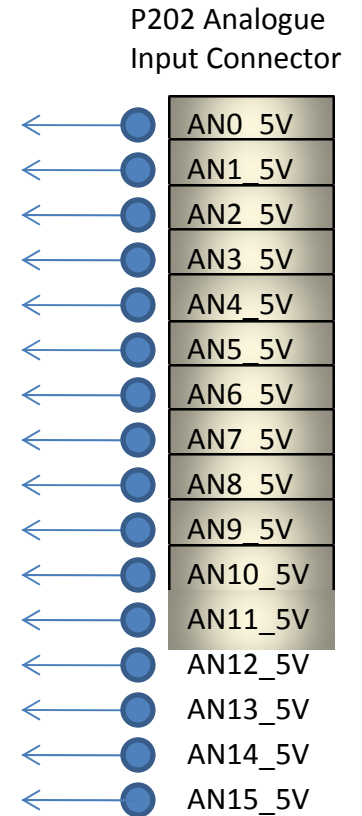
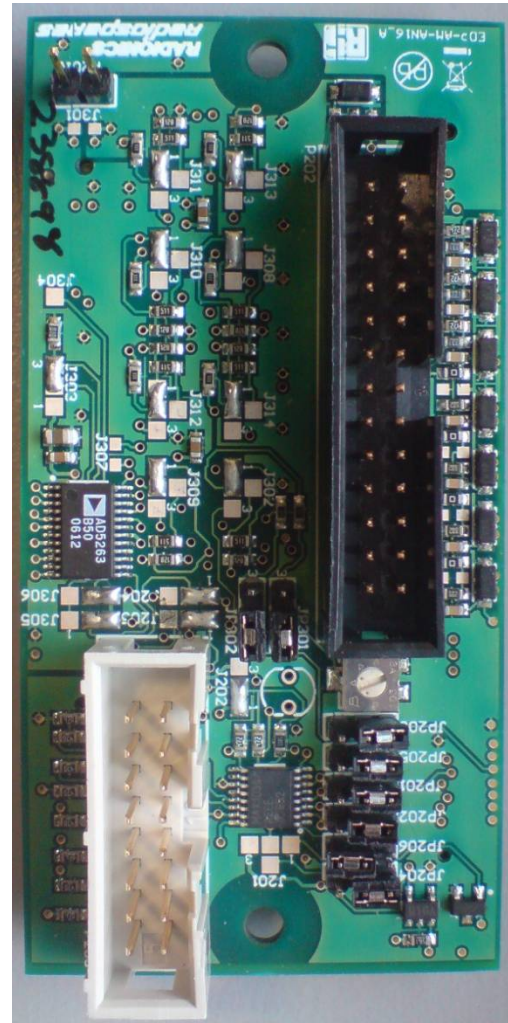
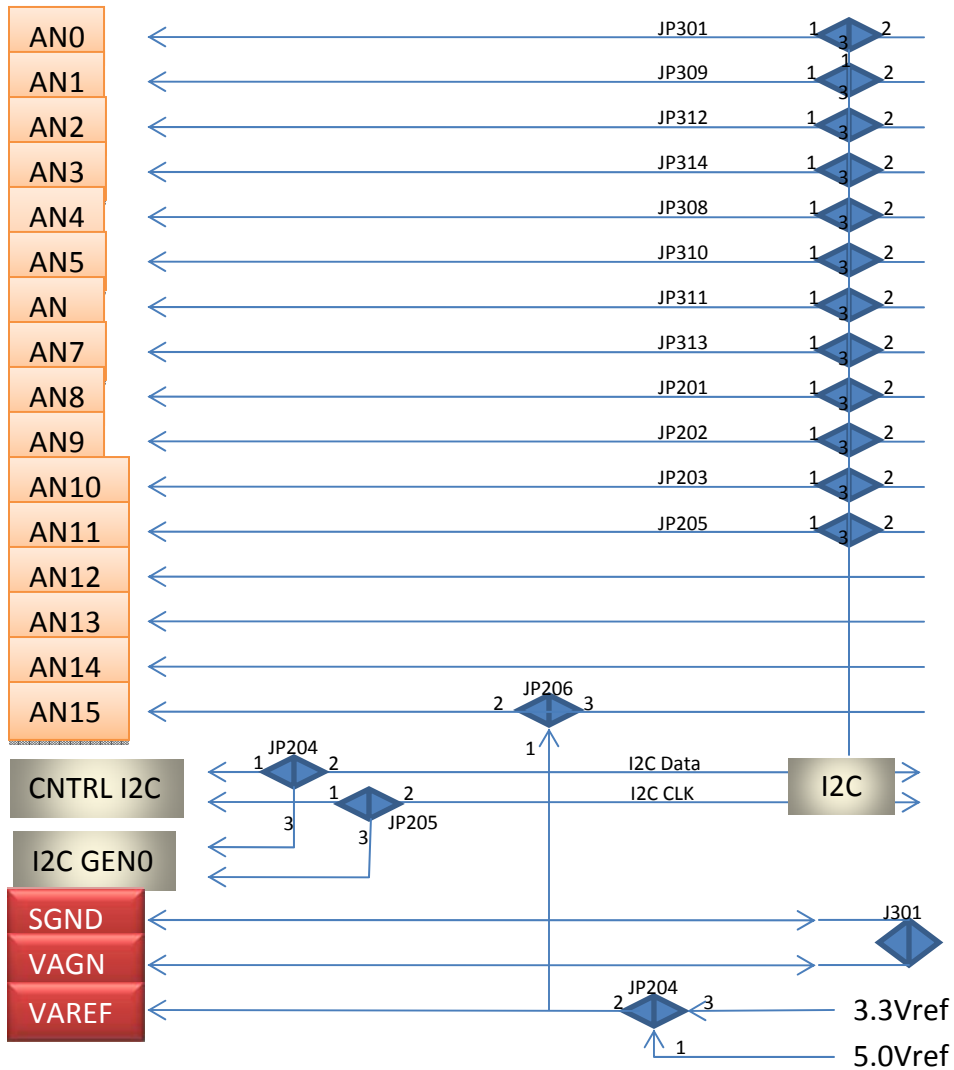


Mapping Aid For RS-EDP Platform



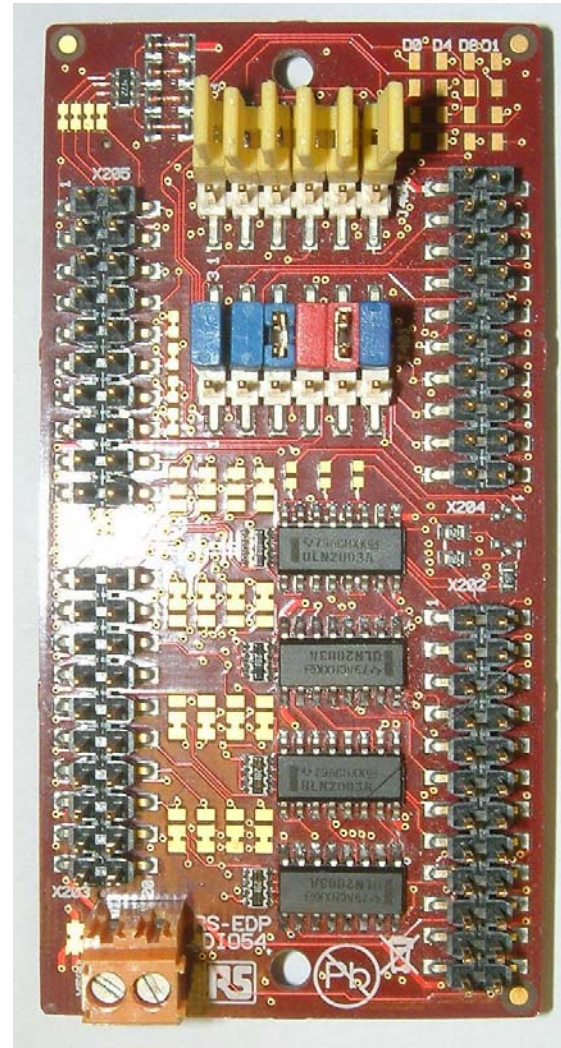
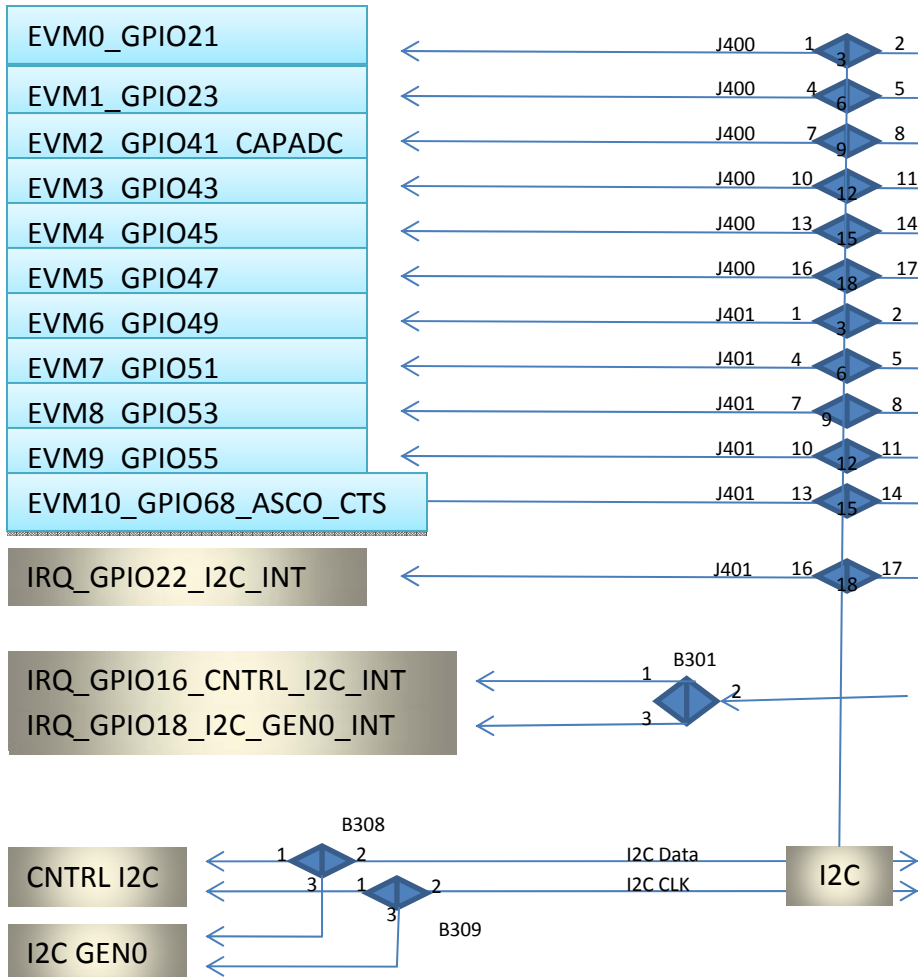
XC167 Mapping Aid Complete - Rev.1

AN16 - Analogue Module to RS-EDP Backplane

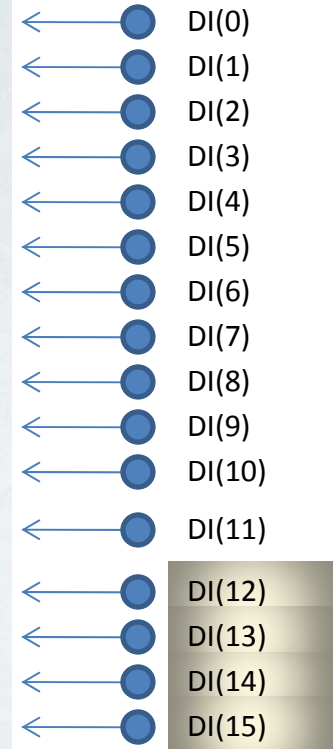


I2C bus can only read inputs AN0_5V to AN11_5V

DIO54 - Digital I/O Module Inputs to RS-EDP Backplane

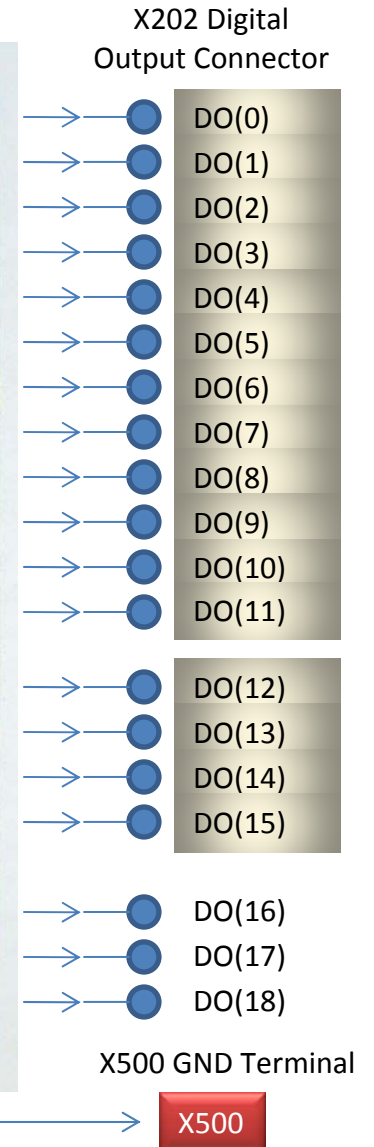
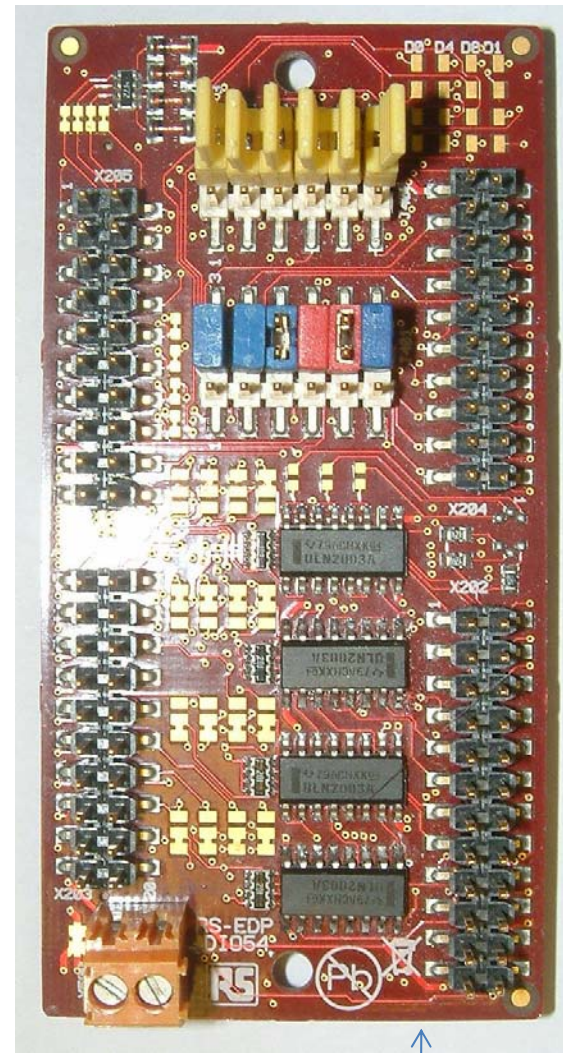
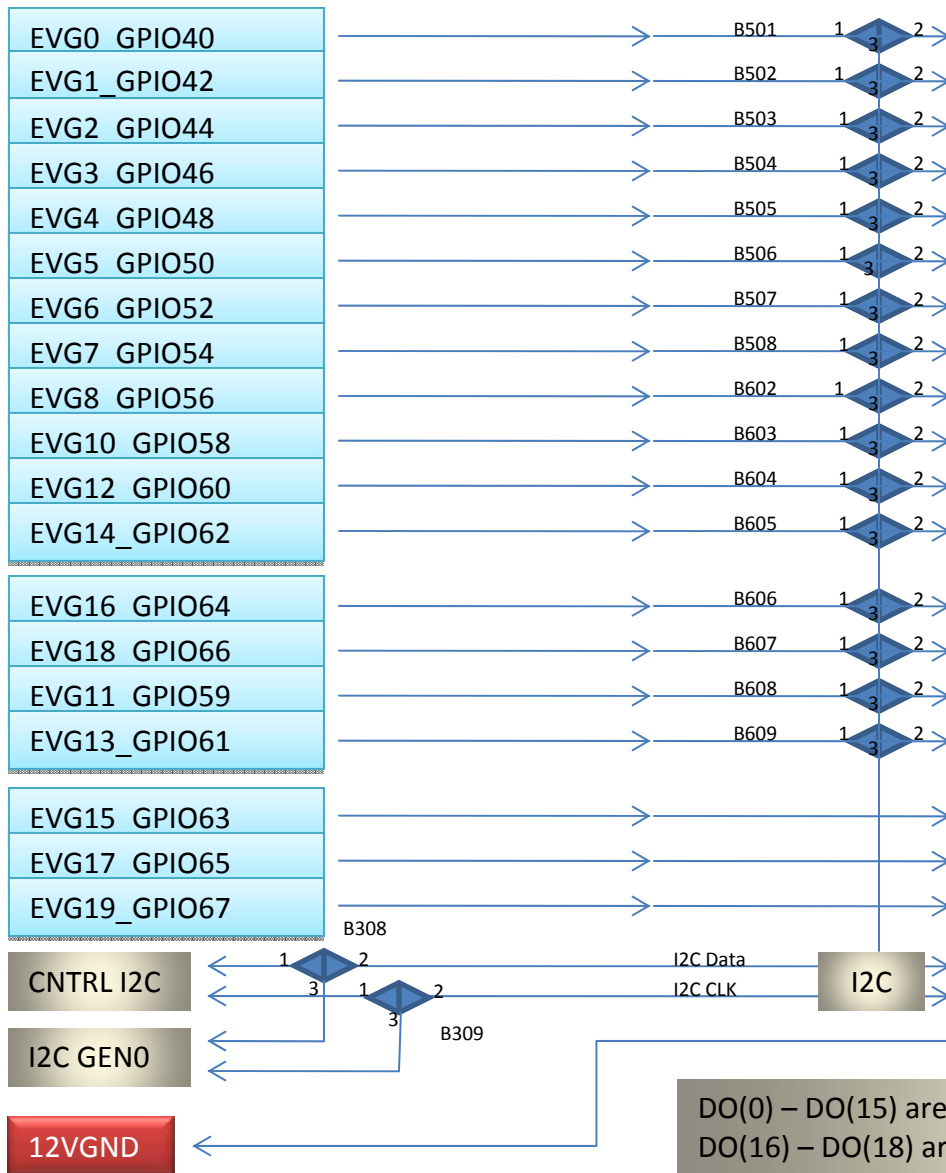


X205 Digital Input Connector



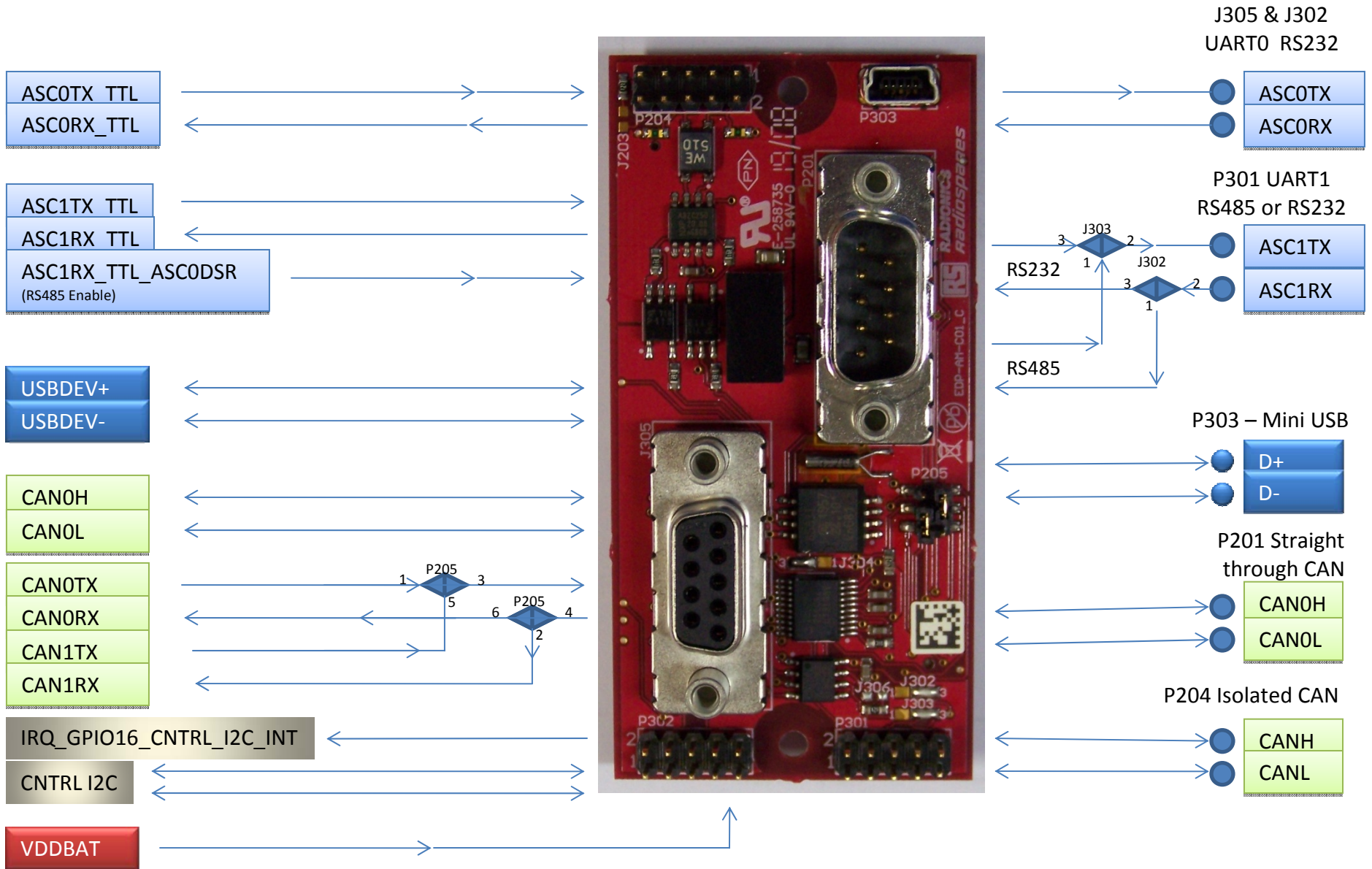
DI(0) – DI(11) can be read via I2C or via backplane by the MCU
 DI(12) – DI(15) can only be read via I2C.

DIO54 - Digital I/O Module Outputs to RS-EDP Backplane

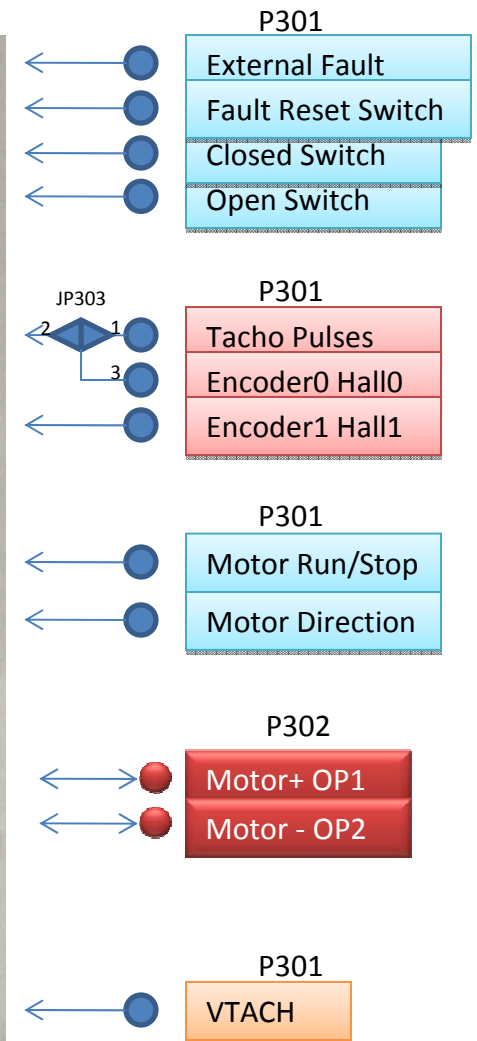
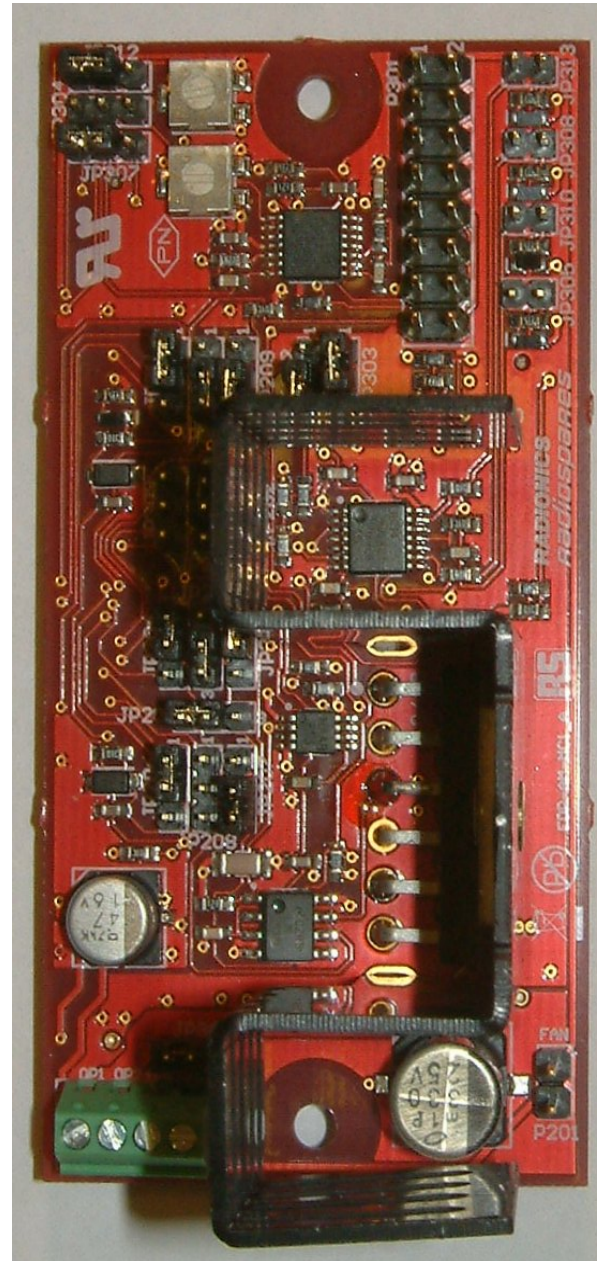
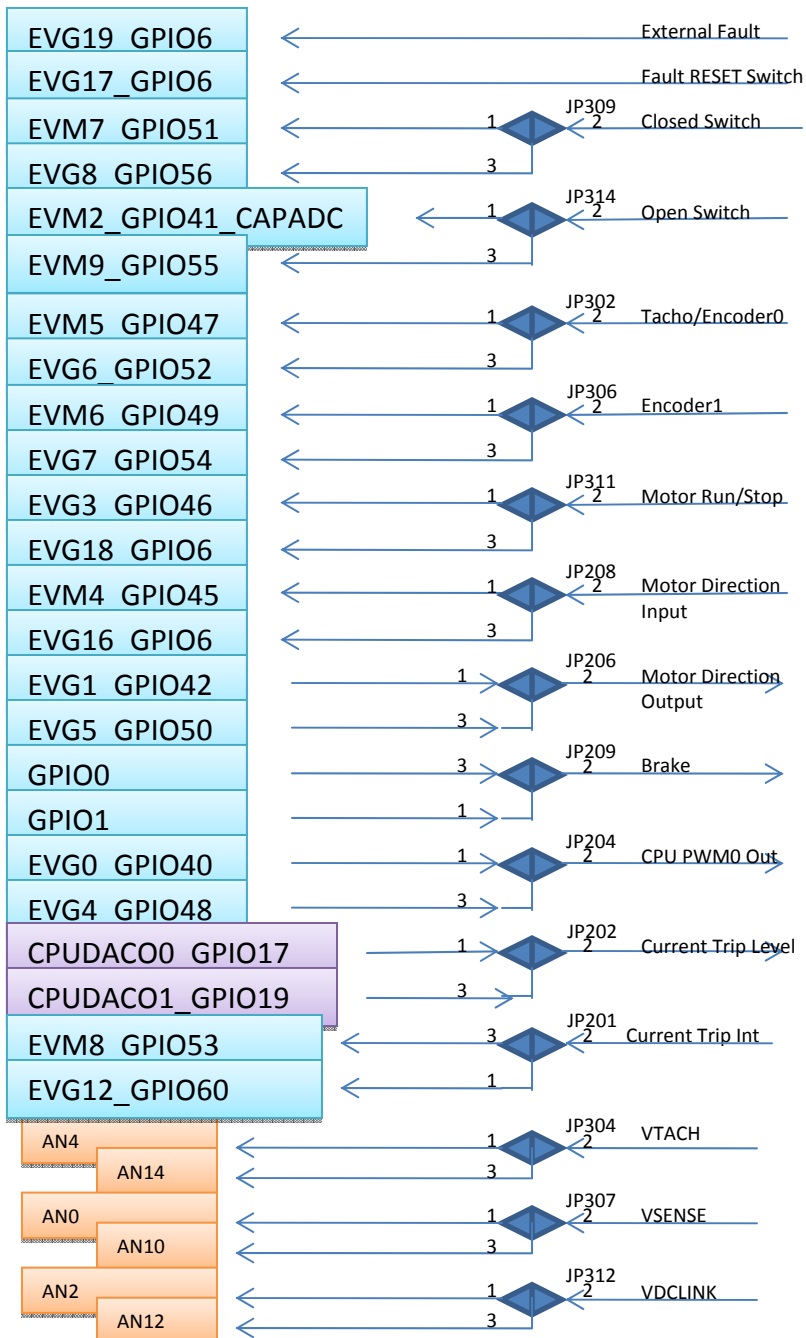


DO(0) – DO(15) are Darlington outputs, controlled by either I2C or direct MCU control
 DO(16) – DO(18) are MCU logic level outputs controlled only by direct MCU

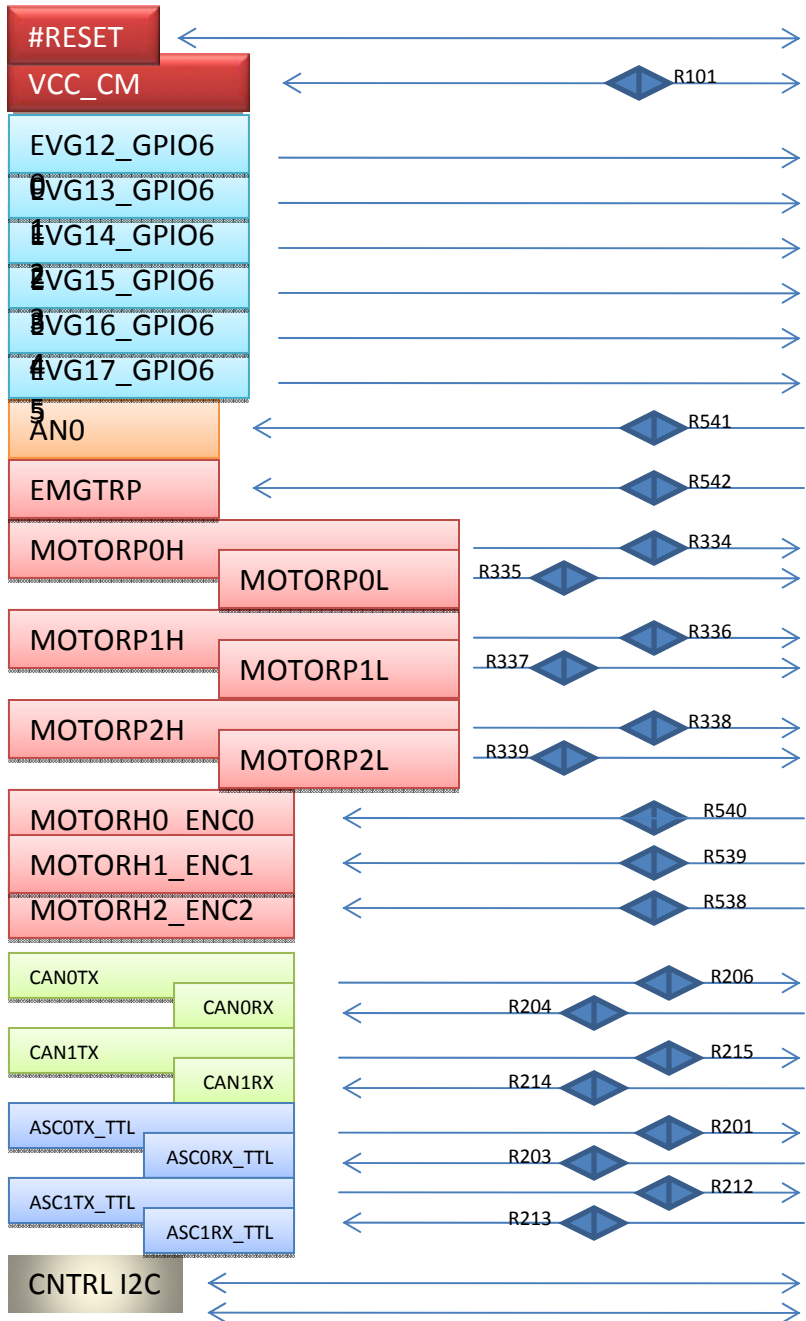
CO1 - Communications Module to RS-EDP Backplane



MC1 - Motor Drive Module to RS-EDP Backplane

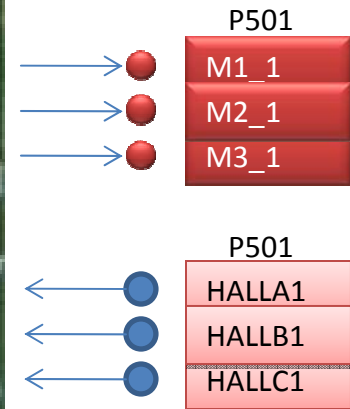


MC2 - Motor Drive Module to RS-EDP Backplane

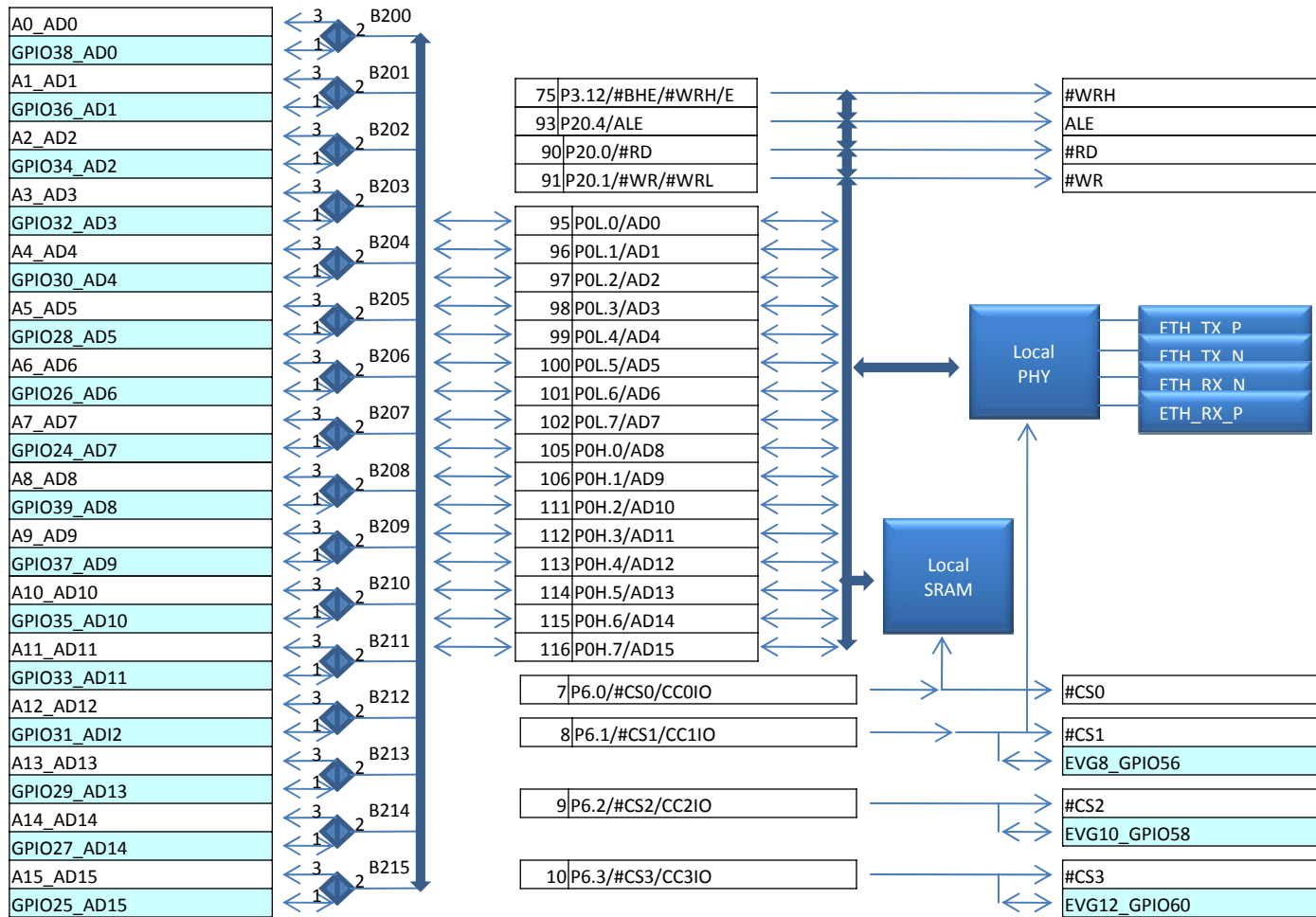


MC2 Module is a twin motor drive module.

Output connections are only shown for the motor under control from an external Command Module, via the backplane.



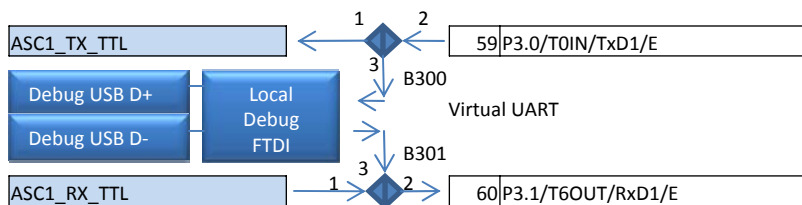
XC167 Command Module



XC167 Command Module

AN0	→	29	P5.0/AN0
AN1	→	30	P5.1/AN1
AN2	→	31	P5.2/AN2
AN3	→	32	P5.3/AN3
AN4	→	33	P5.4/AN4
AN5	→	34	P5.5/AN5
AN6	→	39	P5.6/AN6
AN7	→	40	P5.7/AN7
AN8	→	37	P5.8/AN8
AN9	→	38	P5.9/AN9
AN10	→	35	P5.10/AN10/T6EUD
AN11	→	36	P5.11/AN11/T5EUD
AN12	→	43	P5.12/AN12/T6IN
AN13	→	44	P5.13/AN13/T5IN
AN14	→	45	P5.14/AN14/T4EUD
AN15	→	46	P5.15/AN15/T2EUD

ASCO_TX_TTL	←	69	P3.10/TxD0/E
ASCO_RX_TTL	→	70	P3.11/RxD0/E

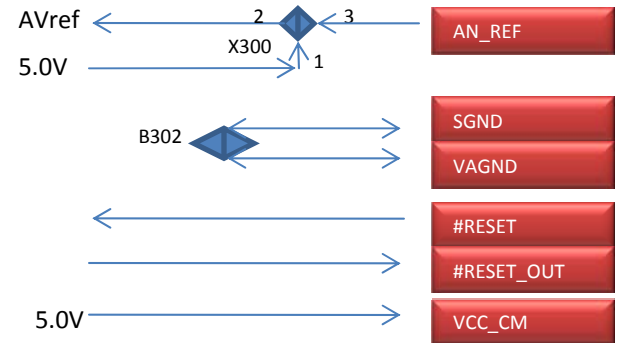


CNTRL_SPI_MRST	→	67	P3.8/MRST0
CNTRL_SPI_MTSR	←	68	P3.9/MTSR0
CNTRL_SPI_CLK	←	76	P3.13/SCLK0/E
CNTRL_SPI_#CS_NSS	←	82	P4.2/A18

SPI_SSC_MRST_MISO	←	128	P1H.1/A9/#CC6POS1/MRST1
MOTORH1_ENC1	↔		
GPIO2_MCI_DAT0	↔		

SPI_SSC_MTSR_MOSI	←	129	P1H.2/A10/#CC6POS2/MTSR1
MOTORH2_ENC2	↔		
GPIO8_MCI_DAT3	↔		

SPI_SSC_CLK	←	130	P1H.3/A11/SCLK1/E
GPIO10_MCI_CLK	↔		
SPI_SSC_#CS_NSS	←	83	P4.3/A19
GPIO14_MCI_PWR	↔		



64	P3.5/T4IN	←	IRQ_GPIO18_I2C_GEN0_INT
65	P3.6/T3IN	←	GPIO5_I2S_TX_WS
66	P3.7/T2IN	←	IRQ_GPIO20_I2C_GEN1_INT
		←	GPIO7_I2S_RX_CLK
		←	IRQ_GPIO22_I2C_INT
		←	GPIO9_I2S_RX_WS
23	P9.2/SDA1/CC18IO/C	←	I2C_GEN0_SDA
24	P9.3/SCL1/CC19IO/C	←	EVG11_GPIO59
		←	I2C_GEN0_SCL
		←	EVG9_GPIO57
25	P9.4/SDA2/CC20IO	←	CNTRL_I2C_SDA
26	P9.5/SCL2/CC21IO	←	CPU_DAC00_GPIO17
		←	CNTRL_I2C_SCL
		←	CPU_DAC01_GPIO19

117	P1L.0/A0/CC60	→	MOTORPOL
118	P1L.1/A1/COU60	→	MOTORPOH
119	P1L.2/A2/CC61	→	MOTORP1L
120	P1L.3/A3/COU61	→	MOTORP1H
121	P1L.4/A4/CC62	←	MOTORP2L
		↔	EVG20_GPIO69_ASCO_RTS
122	P1L.5/A5/COU62	→	MOTORP2H
123	P1L.6/A6/COU63	→	MOTORPWM
124	P1L.7/AD7/#CTRAP/CC22IO	←	EMG_TRAP
		↔	EVM10_GPIO68_ASCO_CTS
		↔	EVM1_GPIO23
61	P3.2/CAPIN	←	IRQ_GPIO16_CNTRL_I2C_INT
		←	MOTOR_TCO_FB
127	P1H.0/A8/#CC6POS0/CC23IO/E	←	EVM0_GPIO21
		←	MOTORH0_ENCO

XC167 Command Module

